

- ④ Pending
- ④ Active:
 - ↳ L18: (443) security adj2 bit
 - ↳ L19: (38) security adj2 row
 - ↳ L22: (9) 18 with unlock\$4
 - ↳ L21: (20) 18 with lock\$4
 - ↳ L23: (4) 22 not 21
 - ↳ L24: (86) 18 with memory
 - ↳ L34: (0) 18 and 365/196
 - ↳ L33: (12) 18 and 365/195
 - ↳ L35: (544) 365/196
 - ↳ L36: (1252) 365/195
 - ↳ L37: (476) 365/185,04
 - ↳ L38: (2280) 365/185,33
 - ↳ L39: (66) 365/ and 18
 - ↳ L40: (42) 39 not 24
 - ↳ L41: (8277) (prevent\$4 or prohibit\$4) with access\$4 with memory
 - ↳ L43: (116) 41 and 36
 - ↳ L44: (53) 41 and 37
 - ↳ L45: (51) 41 and 38
 - ↳ L42: (28) 41 and 35
 - ↳ L46: (101) 43 not (42 or 24)
 - ↳ L47: (46) 44 not (24 or 42)
 - ↳ L49: (40) 45 not 44
 - ↳ L50: (1663) 41 and 365/
 - ↳ L51: (20) 18 and 50
 - ↳ L52: (3) (CURRY near2 DUNCAN).in.

Failed

Saved

U	I	Document ID	Issue Date	Inventor	Current GR	Current XRef Retrieval C	Title	Page	S	C	P	...
1	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 5642460 A	19970624	Brownlee, Paul M. et al.	713/200	305/189.09; 365/195	Method and apparatus for enhanced security of a data...	11	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
2	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6076149 A	20000613	Usami, Tadashi et al.	711/163	326/8; 711/103	Programmable logic device using a two-bit security sche...	11	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>